



Discussion Topics: CF2, Suspend mode, keep I/O high

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Question

How do I keep an I/O pin high while putting the CF2 in suspend mode? (I am using the I/O line to control the power to an external device and want the external device to be powered up while the CF2 is sleeping.)

Suggestion

The circuit below will make DEV_CTRL_IN the inverse of the logic level of a generic CF2 I/O pin (CF2IO). Use PIOSet and PIOClear to control the DEV_CTRL_IN input while the CF2 is running. CF2IO will also go low during suspend turning on the FET and pulling DEV_CTRL_IN high. The transistor is any generic P-channel logic-level enhancement mode MOSFET.

